

FACULTY OF ELECTRICAL ENGINEERING
TIMETABLE FOR SEMESTER 1 SESSION 2023/2024

CAPSTONE PROJECT - VLSI SYSTEM DESIGN LAB

	2	3	4	5	6	7	8	9	10	11	12	13
	8.00-8.50	9.00-9.50	10.00-10.50	11.00-11.50	12.00-12.50	13.00-13.50	1400-1450	1500-1550	1600-1650	1700-1750	1800-1850	2000-2250
SUN						B						
MON		SEEE4743/SEEL4723/SEEM4743(01) DR. AB AL-HADI AB RAHMAN DR. JAMALUDDIN BIN ZAKARIA				R	SEEE4743/SEEL4723/SEEM4743(02) DR. MOHD SHAHRIZAL BIN RUSLI					
TUES						E	CO-CURRICULUM					
WED						A						
THURS						K						